Power Quality Enhancing Systems

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1. Objectives

Discuss the problems related to harmonics in the power system and present some methods to reduce the problems.

- Single-phase diode rectifiers and power factor correctors
- Three-phase diode rectifiers
- Controlled three-phase diode rectifiers: Minnesota and Vienna
- The voltage source converter
- Series and shunt active filters, the unified power flow controller
- Detection and identification of harmonic currents
- Effect of blanking time and forward voltage drop
- Multiple rotating coordinate systems
- Simulation and measurement results



Acronyms

- APF Active Power Filter
- UPFC Unified Power Flow Controller
- SVC Static Var Converter
- HVDC High Voltage Direct Current

All made to improve "Power Quality"



Non-ideal loads

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· are loads that:

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- are non-resistive -> consume reactive power
- vary with time or phase -> consume harmonic current components.
- are different in different phases -> consume negative sequence currents



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2. Single-phase diode rectifiers (I)

A single-phase diode rectifier draws a line current with a large harmonic content since the diodes must be forward biased to conduct.



2. Single-phase diode rectifiers (II)

The current harmonics create voltage harmonics at the point of common coupling (PCC), i.e. at the point where other loads are connected. Note that all single phase diode rectifiers draws current in the vicinity of the peak voltage.



2. Power Factor Corrector (PFC)

Implemented with tolerance-band (or hysteresis) current controller.







3. Three-Phase diode rectifiers

Note that for three-phase diode rectifiers, only two phases carry current simultaneously. This means that each diode has a maximum conduction interval equal to 120° for each half period. Therefore a three-phase PFC is not possible to implement. However there are two types of controllable diode rectifiers derived from three single-phase diode rectifiers (which is also possible to implement):

- The Minnesota rectifier invented by Ned Mohan
- The Vienna rectifier invented by Johann Kolar



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3. The Minnesota Rectifier

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Fig. 3 Proposed Rectification Approach. Capacitors fo

ripple-current component are not shown

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0.48

Fig. 8. Utility-Side Waveforms in the proposed rectifier.

Seconds

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5'th and 7'th harmonic example with Simulink

$$\vec{i} \,^{\alpha\beta} = \sqrt{\frac{3}{2}} \cdot \hat{i}_1 \cdot e^{j(\omega t - \varphi)} + \sqrt{\frac{3}{2}} \cdot \hat{i}_5 \cdot e^{j(-5\omega t - \varphi)} + \sqrt{\frac{3}{2}} \cdot \hat{i}_7 \cdot e^{j(7\omega t - \varphi)}$$
$$\vec{i} \,^{dq} = \sqrt{\frac{3}{2}} \cdot \hat{i}_1 \cdot e^{j\left(\frac{\pi}{2} - \varphi\right)} + \sqrt{\frac{3}{2}} \cdot \hat{i}_5 \cdot e^{j(-6\omega t - \varphi)} + \sqrt{\frac{3}{2}} \cdot \hat{i}_7 \cdot e^{j(6\omega t - \varphi)}$$



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Types of Active Filters



Active Filter Combinations Load Load Shunt Shunt Shunt AF **Passive Filter Passive Filter** Series AF VAF iLine iLoad -IAF Load Series AF Shunt AF Shunt and series hybrid active filters (top) and Unified Power Flow Controller, UPFC (bottom). L7&L8 – Power Quality Enhancing Systems © Mats Alaküla





DC link Voltage Control System (I)



DC link Voltage Control System (II)



• Transfer function from reference to actual DC link voltage $(U_{dc}^*(s)-U_{dc}(s))\cdot K_{p,dc}\cdot (1+\frac{1}{sT})\cdot \frac{1}{1+sT}\cdot \frac{1}{sC} = U_{dc}(s) \Leftrightarrow$

$$G_{Udc}(s) = \frac{U_{dc}(s)}{U_{dc}^*(s)} = \frac{s^3 + (1/T_s)s^2}{s^3 + (1/T_s)s^2 + (K_{p,dc}/T_sC)s + (K_{p,dc}/T_{i,dc}T_sC)}$$

• **Desired denominator polynomial** $A(s) = (s + \omega_n) \cdot (s^2 + 2\zeta_n \omega_n s + \omega_n^2)$

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Controller Parameters ...

Use Symmetric Optimum

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$$\zeta_n = \frac{a-1}{2}$$
, $\omega_n = \frac{a}{T_s}$ (Note $a > 1$ for stable system) \Rightarrow
 $K_{p.dc} = \frac{a \cdot C}{T_{i.dc}}$, $T_{i.dc} = a^2 \cdot T_s$

• Note that for a = 2 the poles are placed as for a Butterworth filter, i.e. maximum flat pass-band. However, the bandwidth is by far to high for practical operation.



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Convert DC to AC current references

 $p(t) = Ri_{1d}^2 + Ri_{1q}^2 + L\frac{di_{1d}}{dt}i_{1d} + L\frac{di_{1q}}{dt}i_{1q} + e_{cp,q}i_{1q} = u_{dc} \cdot i_{dc1} \approx e_{cp,q}i_{1q} \implies$ $i_{dc1} = \frac{e_{cp.q}}{u_{dc}} \cdot i_{1q} \implies i_{1q}^* = \frac{u_{dc}}{e_{cp.q}} \cdot i_{dc1}^* = \frac{u_{dc}}{e_{cp.q}} \cdot \left(i_{dc2} - i_{Cdc}^*\right)$



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Example with active filtering



Filter Current References (I)

 $i_{d,ActiveFilter}^* = i_{d,load}$

$$i_{q,ActiveFilter}^{*} = i_{q,load} \cdot \frac{s \cdot T_{f}}{1 + s \cdot T_{f}} + \frac{u_{dc}}{e_{cp}} \left(i_{dc2} - K_{p,dc} \cdot \left(1 + \frac{1}{sT_{i,dc}} \right) \cdot \left(u_{dc}^{*} - u_{dc} \right) \right) \cdot \frac{1}{1 + s \cdot T_{f}}$$

- Note that the q-direction current reference based on the load current is high-pass filtered since the active filter should not provide active power to the load
- Note that the DC link current reference is low- pass filtered since it is important that it does not contain any 100 Hz (twice fundamental) component which would result in a negative sequence component consumed by the active filter

Filter Current References (II)

- To avoid that the DC link current reference contains a negative sequence component $T_f \le 10$ ms is a proper choice.
- To assure that the negative sequence current consumed by the load is provided by the active filter *T_f* ≥ 10 ms is a proper choice.
- Use $T_f = 10 \text{ ms}$

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Filter Current References (III)

- Since T_f = 10 ms >> T_s ≈ 100 μs (typically) the converter dynamics do not have to be included in the selection of the DC link voltage controller parameters, i.e. G_i(s) = 1.
- However a new low-pass filter shows up in the transfer function, e.g. the one with time constant T_f .
- This means that the transfer function looks exactly the same but *T_s* is replaced by *T_f*.
- In this case *a* = 2 is a proper choice for the DC link voltage controller parameters.
- Note that a = 3 gives real poles since the closed loop damping of all poles is equal to 1.
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- The rather long time constant of the low-pass filter (*T_j*) results in the need of a comparably large DC link capacitor (*C*) for converters used for active filters.
- In this case the DC side load current (*i*_{dc2}) is also filtered with *T_f* which increases the need of a large DC link capacitor. The DC side load current (*i*_{dc2}) should be fed forward without any low-pass filtering for proper operation.

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X. Effects of non-idealities (V)

The three-phase converter controller can not fully compensate for these non-idealities for two reasons:

- The proportional part of the controller is too "weak" to compensate. The only possible way to make it less weak is to increase the gain which would give an oscillatory behaviour
- The integral part act properly only on dc-components and not on dq-reference frame harmonics. To circumvent the last problem, rotating coordinate systems are included also for the frequencies where the harmonics appear.



More Information

Active Power Line Conditioners are in most cases not covered in books on power electronics, however:

On the home page of power electronics course:

• The Power Electronics Handbook", edited by Timothy L Skvarenina, CRC-press, 2002, ISBN 0-8493-7336-0. http://www.engnetbase.com/books/447/7336_pdf_toc.pdf

On Per Karlsson's homepage:

• M. Bojrup, (1999), "Advanced Control of Active Filters in a Battery Charger Application", Licentiate thesis, Department of Industrial Electrical Engineering and Automation, Lund Institute of Technology, Lund, Sweden, December 1999, ISBN 91-88934-13-6. http://www.iea.lth.se/~ielper/charger/MB-thesis.pdf



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Y. Conclusions

Commercial Semikron three-phase inverter Suitable also for active power line conditioning!



V_{DC-link} = 600 V ± 10 % t = 3 x 420 V (0,05 Hz ... 60 Hz) 100 A_{RMS}, I_{overload} = 165 A_{RMS} switching = 1,5 kHz

> SKiiP 342GD120 - 314 CTV P 16/280 F 9 900 uE / 800 V at DC-link



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