


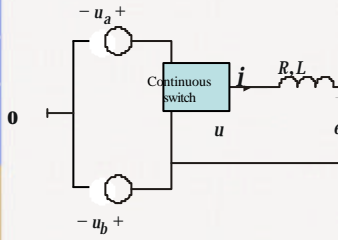
Modulation

Industrial Electrical Engineering and Automation
Lund University, Sweden



Why switching?

- Continuous amplifiers have low efficiency



Continuous amplifier circuit diagram showing input voltage u , output voltage u_a , and output current i through a load R, L .

$$P_{ut}(t) = u \cdot i$$

$$P_{in}(t) = (u_a - u_b) \cdot i$$

Antag:

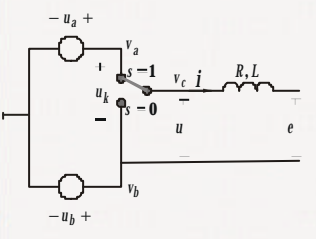
$$(u_a - u_b) = U_{dc}$$

$$P_{in} = U_{dc} \cdot i$$

$$h = \frac{P_{ut}}{P_{in}} = \frac{u \cdot i}{U_{dc} \cdot i} = \frac{u}{U_{dc}}$$

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Switching amp. Losses



Switching amplifier circuit diagram showing input voltage u , output voltage u_a , and output current i through a load R, L . The switch s is controlled by v_c .

$$u = \begin{cases} 0 & \text{when } s = 0 \\ U_{dc} & \text{when } s = 1 \end{cases}$$

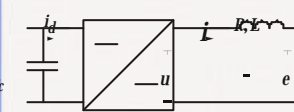
$$P_{in} = \begin{cases} 0 & \text{when } s = 0 \\ u \cdot i & \text{when } s = 1 \end{cases}$$

$$P_{ut} = \begin{cases} 0 & \text{when } s = 0 \\ u \cdot i & \text{when } s = 1 \end{cases}$$

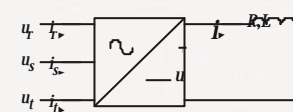
$$h = \frac{P_{ut}}{P_{in}} = \begin{cases} \text{undefined} & \text{when } s = 0 \\ 100\% & \text{when } s = 1 \end{cases}$$

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Converter topology



Buck converter topology diagram showing input voltage u and output voltage u_t through a load R, L .



Boost converter topology diagram showing input voltage u and output voltage u_t through a load R, L .

- 1 side capacitive
- 1 side inductive
- ALWAYS!

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Modulation - Control of voltage time area

$$v_c = \begin{cases} v_a & \text{when } s = 1 \\ v_b & \text{when } s = 0 \end{cases}$$

$$u = s \cdot (v_a - v_b) = s \cdot u_k = \begin{cases} u_k & \text{when } s = 1 \\ 0 & \text{when } s = 0 \end{cases}$$

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Output voltage

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Voltage control options

Assume a limited pulse interval T and a slowly varying switching voltage u_k

$$Y_0 = \int_0^T u_k(t) \cdot dt$$

$$u_k(t_+) = \frac{dy(t_+)}{dt_+}$$

$$u_k(t_-) = \frac{dy(t_-)}{dt_-}$$

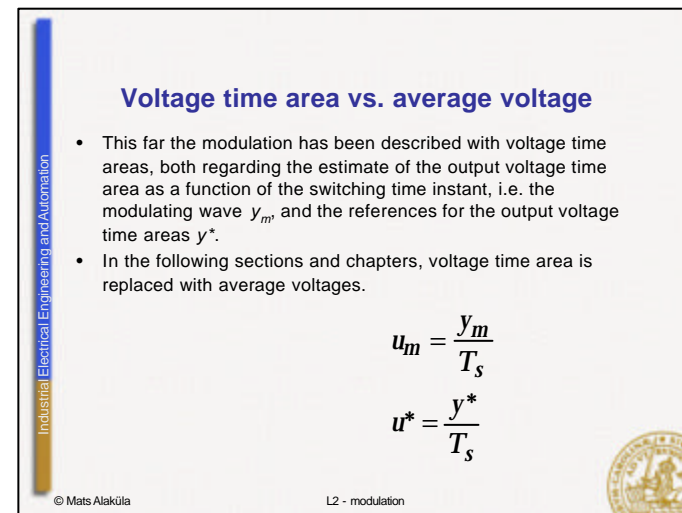
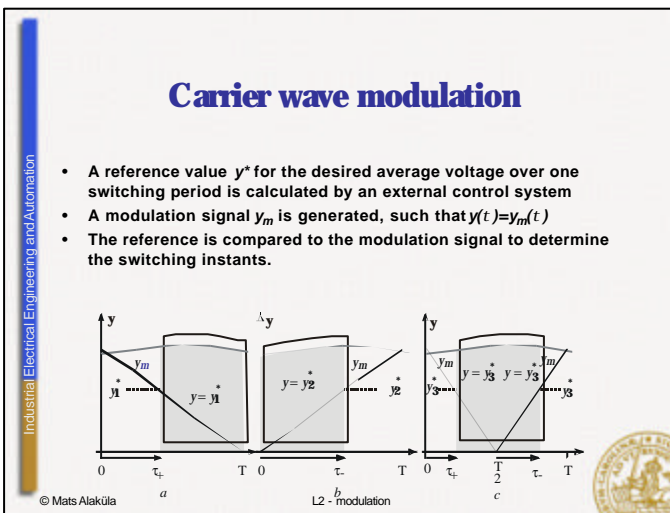
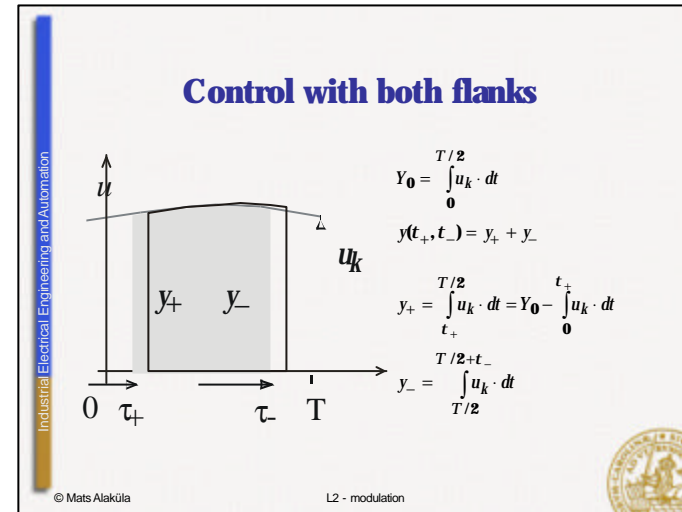
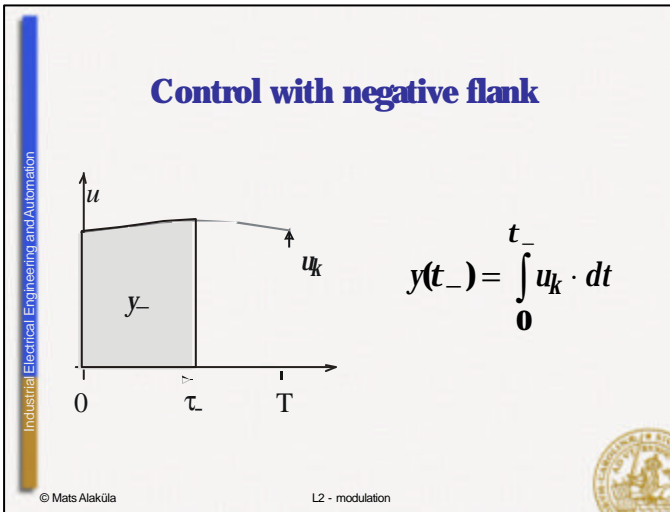
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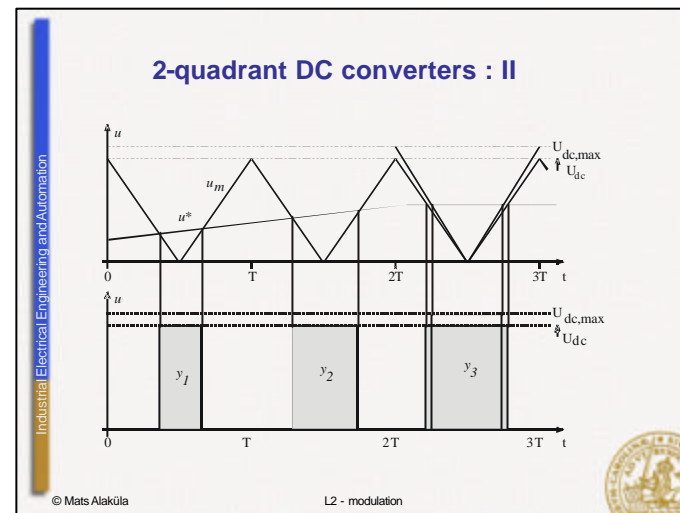
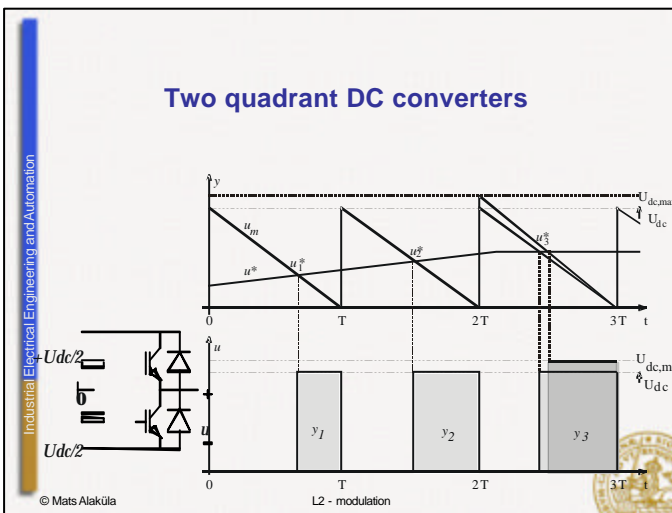
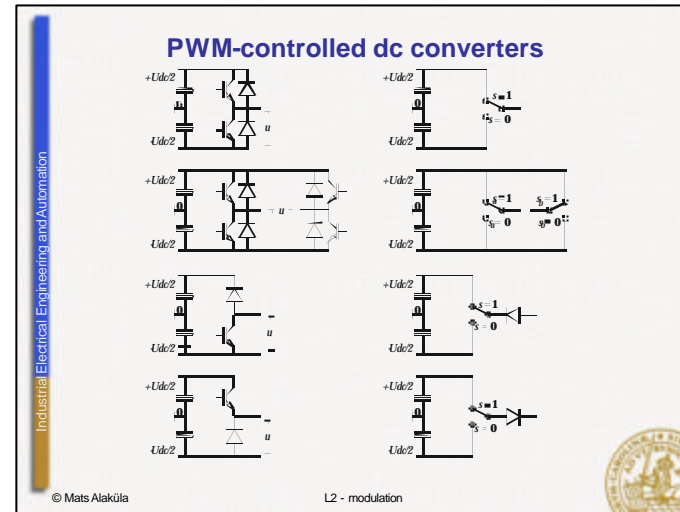
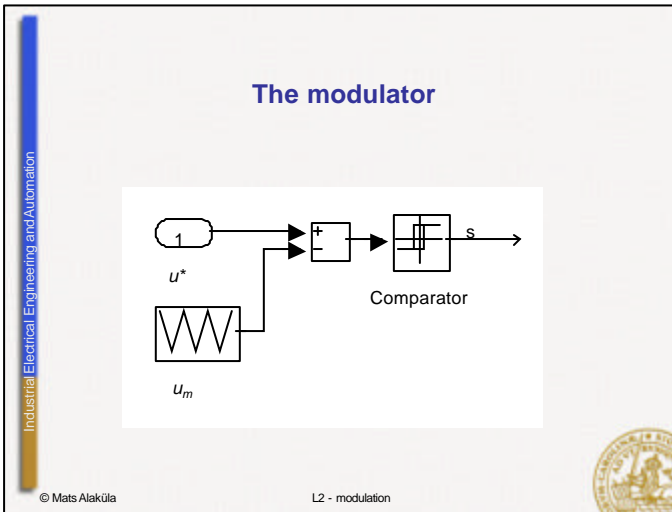
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Control with positive flank

$$y(t_+) = \int_{t_+}^T u_k \cdot dt = Y_0 - \int_0^{t_+} u_k \cdot dt$$

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2-quadrant DC converters : III

Current sampling – how often?

- When the carrier turns, i.e. With twice the switching frequency!

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Example

- Sampling of current

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4 – quadrant DC converters

Bridge connected
2 phase potentials, only 1 output voltage = 1 degree of freedom to be used for other purposes.

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4-quadrant DC converters

$$u^* = v_a^* - v_b^*$$

$$\text{alt 1: } v_a^* = \text{sign}(u^*) \cdot \frac{U_{dc}}{2} \Rightarrow v_b^* = v_a^* - u^* = \text{sign}(u^*) \cdot \frac{U_{dc}}{2} - u^*$$

$$\text{alt 2: } v_a^* = -v_b^* \Rightarrow v_a^* - v_b^* = 2 \cdot v_a^* \Rightarrow \begin{cases} v_a^* = \frac{u^*}{2} \\ v_b^* = -\frac{u^*}{2} \end{cases}$$

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